**March 1st Senior Project Meeting**

**Armstrong Hall 148: 2:00PM – 2:30PM**

**Members in Attendance:** Zach, Dhruvit, Kevin, Whitley, Dr. Hernandez, and Dr. Pearlstein

* Zach
  + Last Week
    - Finished TCF Poster
    - Learned how to use Plan Ahead software
    - Set up clock signal and specified I/O ports
    - Fixed Realistic Constraints Form
  + This Week
    - Warnings for Xilinx ISE
    - Finish design in Plan Ahead
    - Get poster printed
* Dhruvit
  + Last Week
    - Top-Level Simulation
    - Looked at Physical Design presentation
  + This Week
    - Get a plan for the physical design
* Julie
  + Last Week
    - Looked at Physical Design presentation
  + This Week
    - Get a plan for the physical design
* Kevin
  + Last Week
    - Top-Level Simulation
    - Text file comparison
  + This Week
    - Install Model-Sim (Dhruvit might help)
    - Fix warnings
    - Clean up code
* Whitley
  + Last Week
    - Top-Level Simulation
  + This Week
    - I2C PSoC Program
    - Fix I2C always block
* Automate testing process using make file